

LAN9220 / LAN9221 Mini Development Card

Design Details

Board:
Assy 6527

Chip:
SMSC LAN9220 / LAN9221

Board Form Factor:
59.60mm x 58.42mm

Assembly:
56-Pin QFN

Assy 6527

PCB Revision B

Schematic Revision 1.1

Circuit Diagrams utilizing SMSC Products Are Included As A Means Of Illustrating Typical Semiconductor Applications: Consequently Complete Information Sufficient For Construction Purposes Is Not Necessarily Given. The Information Has Been Carefully Checked And Is Believed To Be Entirely Reliable. However, No Responsibility Is Assumed For Inaccuracies. Furthermore, Such Information Does Not Convey To The Purchaser Of The Semiconductor Devices Described Any License Under The Patent Rights Of SMSC Or Others. SMSC Reserves The Right To Make Changes At Any Time In Order To Improve Design And Supply The Best Product Possible.

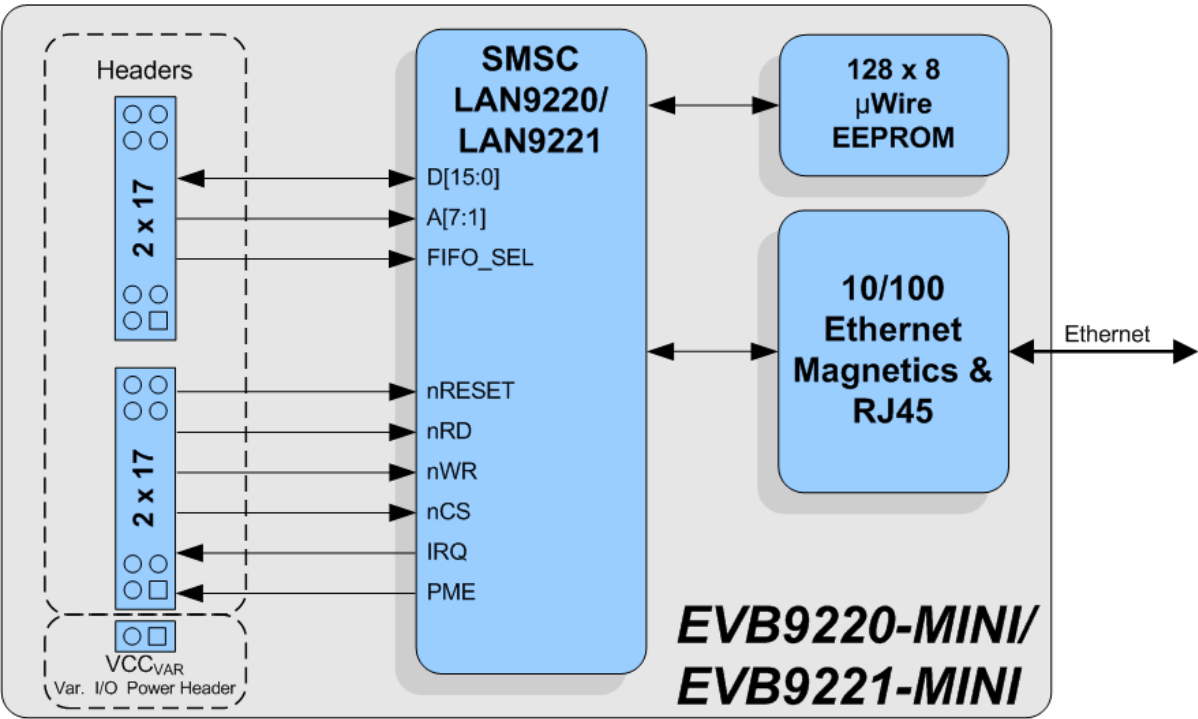
ITEM	Page(s)
Title Page	1
Expansion Slot Edge Fingers	2
LAN9220 / LAN9221	3

Revision History

Rev 1.0 to 1.1:

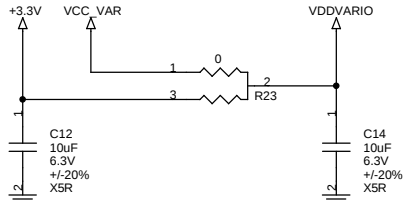
Page 2: Added note for default ADDR1 configuration

BLOCK DIAGRAM



Title Page				
Size	Engineer	Assembly No.	PCB Rev	Schematic Rev
C	J. M.	6527	B	1.1
Date:	Monday, September 14, 2009		Sheet	1 of 3

NOTE:
Pins 1 & 2 are used to detect the presence of an expansion card.



The left diagram shows a 2x17 memory module connected to a J1 header. The module's pins are connected to the header pins as follows:

- Pin 1: +3.3V
- Pin 2: RISER_RSVD7
- Pin 3: RISER_RSVD3
- Pin 4: RISER_RSVD6
- Pin 5: RISER_RSVD1
- Pin 6: RISER_RSVD2
- Pin 7: ADDR11
- Pin 8: ADDR7
- Pin 9: ADDR4
- Pin 10: ADDR3
- Pin 11: DATA14
- Pin 12: DATA10
- Pin 13: DATA6
- Pin 14: DATA2
- Pin 15: IRQ
- Pin 16: DATA13
- Pin 17: DATA9
- Pin 18: DATA5
- Pin 19: DATA1
- Pin 20: nRD
- Pin 21: nWE
- Pin 22: nRST
- Pin 23: nCS
- Pin 24: nCS
- Pin 25: nCS
- Pin 26: nCS
- Pin 27: nCS
- Pin 28: nCS
- Pin 29: nCS
- Pin 30: nCS
- Pin 31: nCS
- Pin 32: nCS
- Pin 33: nCS
- Pin 34: nCS
- Pin 35: nCS
- Pin 36: nCS
- Pin 37: nCS
- Pin 38: nCS
- Pin 39: nCS
- Pin 40: nCS
- Pin 41: nCS
- Pin 42: nCS
- Pin 43: nCS
- Pin 44: nCS
- Pin 45: nCS
- Pin 46: nCS
- Pin 47: nCS
- Pin 48: nCS
- Pin 49: nCS
- Pin 50: nCS
- Pin 51: nCS
- Pin 52: nCS
- Pin 53: nCS
- Pin 54: nCS
- Pin 55: nCS
- Pin 56: nCS
- Pin 57: nCS
- Pin 58: nCS
- Pin 59: nCS
- Pin 60: nCS
- Pin 61: nCS
- Pin 62: nCS
- Pin 63: nCS
- Pin 64: nCS
- Pin 65: nCS
- Pin 66: nCS
- Pin 67: nCS
- Pin 68: nCS
- Pin 69: nCS
- Pin 70: nCS
- Pin 71: nCS
- Pin 72: nCS
- Pin 73: nCS
- Pin 74: nCS
- Pin 75: nCS
- Pin 76: nCS
- Pin 77: nCS
- Pin 78: nCS
- Pin 79: nCS
- Pin 80: nCS
- Pin 81: nCS
- Pin 82: nCS
- Pin 83: nCS
- Pin 84: nCS
- Pin 85: nCS
- Pin 86: nCS
- Pin 87: nCS
- Pin 88: nCS
- Pin 89: nCS
- Pin 90: nCS
- Pin 91: nCS
- Pin 92: nCS
- Pin 93: nCS
- Pin 94: nCS
- Pin 95: nCS
- Pin 96: nCS
- Pin 97: nCS
- Pin 98: nCS
- Pin 99: nCS
- Pin 100: nCS

The right diagram shows a 2x18 memory module connected to a J2 header. The module's pins are connected to the header pins as follows:

- Pin 1: +3.3V
- Pin 2: RISER_RSVD5
- Pin 3: RISER_RSVD1
- Pin 4: RISER_RSVD4
- Pin 5: RISER_RSVD0
- Pin 6: ADDR6
- Pin 7: ADDR2
- Pin 8: ADDR5
- Pin 9: ADDR1_R
- Pin 10: DATA12
- Pin 11: DATA8
- Pin 12: DATA4
- Pin 13: DATA0
- Pin 14: nWE
- Pin 15: nRST
- Pin 16: DATA15
- Pin 17: DATA11
- Pin 18: DATA7
- Pin 19: DATA3
- Pin 20: PME
- Pin 21: nCS
- Pin 22: nCS
- Pin 23: nCS
- Pin 24: nCS
- Pin 25: nCS
- Pin 26: nCS
- Pin 27: nCS
- Pin 28: nCS
- Pin 29: nCS
- Pin 30: nCS
- Pin 31: nCS
- Pin 32: nCS
- Pin 33: nCS
- Pin 34: nCS
- Pin 35: nCS
- Pin 36: nCS
- Pin 37: nCS
- Pin 38: nCS
- Pin 39: nCS
- Pin 40: nCS
- Pin 41: nCS
- Pin 42: nCS
- Pin 43: nCS
- Pin 44: nCS
- Pin 45: nCS
- Pin 46: nCS
- Pin 47: nCS
- Pin 48: nCS
- Pin 49: nCS
- Pin 50: nCS
- Pin 51: nCS
- Pin 52: nCS
- Pin 53: nCS
- Pin 54: nCS
- Pin 55: nCS
- Pin 56: nCS
- Pin 57: nCS
- Pin 58: nCS
- Pin 59: nCS
- Pin 60: nCS
- Pin 61: nCS
- Pin 62: nCS
- Pin 63: nCS
- Pin 64: nCS
- Pin 65: nCS
- Pin 66: nCS
- Pin 67: nCS
- Pin 68: nCS
- Pin 69: nCS
- Pin 70: nCS
- Pin 71: nCS
- Pin 72: nCS
- Pin 73: nCS
- Pin 74: nCS
- Pin 75: nCS
- Pin 76: nCS
- Pin 77: nCS
- Pin 78: nCS
- Pin 79: nCS
- Pin 80: nCS
- Pin 81: nCS
- Pin 82: nCS
- Pin 83: nCS
- Pin 84: nCS
- Pin 85: nCS
- Pin 86: nCS
- Pin 87: nCS
- Pin 88: nCS
- Pin 89: nCS
- Pin 90: nCS
- Pin 91: nCS
- Pin 92: nCS
- Pin 93: nCS
- Pin 94: nCS
- Pin 95: nCS
- Pin 96: nCS
- Pin 97: nCS
- Pin 98: nCS
- Pin 99: nCS
- Pin 100: nCS

